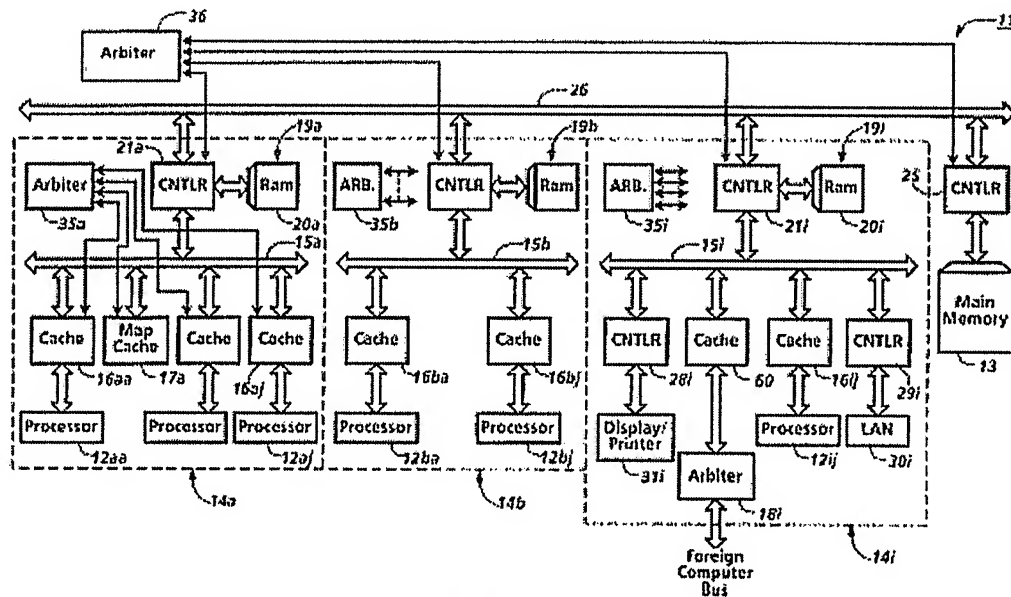


### REMARKS

These remarks are made in response to the Office Action of September 5, 2007 (Office Action). As this response is timely filed within the 3-month shortened statutory period, no fee is believed due. Claims 1-15 are in the present application and have been amended. No new matter has been added.

The Applicants note that the Office Action Summary has checked off an objection to the drawings, but the Office Action does not assert any such objection.

Claims 1-15 were rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent 5,440,698 to Sindhu, et al. (hereinafter "Sindhu"). Claims 1 to 15 include the communication interface, which has a network of nodes and a memory interface, being positioned on a single chip. Sindhu does not disclose or suggest this feature. Sindhu is directed to an arbitration system for packet switched busses as shown in FIG. 1:



**Fig. 1**

Sindhu does not discuss the positioning of the communication interface on a single chip. Rather, Sindhu describes the use of a cluster of processors. (Sindhu col. 4, lines 30-54).

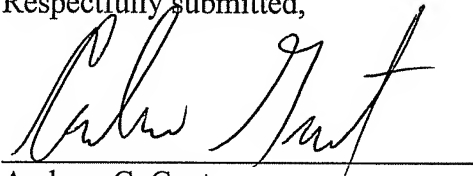
In view of the foregoing, Applicants respectfully submit that the specification, the drawings and all claims presented in this application are currently in condition for allowance. Accordingly, Applicants respectfully request favorable consideration and that this application be passed to allowance.

Should any changes to the claims and/or specification be deemed necessary to place the application in condition for allowance, the Examiner is respectfully requested to contact the undersigned to discuss the same.

Dated: \_\_\_\_\_

11/26/07

Respectfully submitted,



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